

NANO KOREA 2018

July 10~13, KINTEX, Korea

ES Jung

President & General Manager, Foundry Business, Samsung Electronics

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Nationality: Republic of Korea

EDUCATION

University of Texas at Arlington	Ph.D	Physics	1997
Seoul National University	MS	Physics	1985
Seoul National University	BS	Physics Education	1983

PROFESSIONAL ACTIVITIES

- President and General Manager of Samsung Foundry, 2017 to Present
- EVP and Head of Samsung Semiconductor R&D Center, 2012 to 2017
- SVP and Head of System LSI Manufacturing Operation Center, 2011 to 2012

AWARD AND HONORS

- Silver Tower Order of Industrial Service Merit, 2016

MAIN SCIENTIFIC PUBLICATION

- Embedded STT-MRAM in 28nm FDSOI Logic Process for Industrial MCU/IoT Application, IEEE Symposium on VLSI Technology, Jun. 2018
- Highly Manufacturable 7nm CMOS FinFET Technology featuring EUV lithography for Low Power and High Performance Applications, IEEE Transaction on Electron Devices, July 2017
- 3D Packaging Technology Platforms for the Beyond Moore Era, IEEE International Interconnect Technology Conference, May 2017
- 3D-NAND Flash for High-density Memory and its Technology Evolutions and Challenges on the Future, IEEE International Electron Devices Meeting, Dec. 2016
- High Performance Cu/Low-k Interconnect Strategy beyond 10nm Logic Technology, IEEE International Interconnect Technology Conference, May 2015

RESEARCH INTERESTS (Activities)

- Advanced logic process technology (HKMG, FinFET, EUV, GAA, FD-SOI)
- Specialty technologies (eMRAM, CIS, DDI, PMIC, eFlash)
- 48/64 layers V-NAND flash
- 1xnm low power DRAM

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President Dr. ES Jung is the General Manager of Samsung Foundry. As the previous Head of Samsung Semiconductor R&D Center and of System LSI Manufacturing Operation Center, he brings technology, operation and service leadership to Samsung Foundry.

Dr. Jung, during his tenure at Samsung Semiconductor R&D Center, laid the foundation for Samsung Foundry to become the leader in advanced logic and specialty process technologies, including mass production of foundry industry's first 32/28nm high-k, metal gate (HKMG) and 14nm FinFET, world's very first mass production of 10nm FinFET as well as world's first adoption of EUV (Extreme Ultraviolet) lithography and post-FinFET GAA (Gate All-Around) architecture. Moreover, under his management, Samsung's global leadership in DRAM and NAND Flash process technologies has been furthered. Since his appointment as the Head of Samsung Foundry, Samsung Foundry has attracted many new business opportunities and has renewed its DNA as a pure-play foundry.

He is a member of the National Academy of Engineering of Korea and has been awarded the Order of Industrial Service Merit by the Korean government for his contributions to the advancement of technology. He holds a MS in Physics from Seoul National University and a Ph.D. in Physics from the University of Texas at Arlington.